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Network processor with single interfer

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Network processor with single interface supporting tree search engine and CAM

US Pat 7167471 - Filed Aug 28, 2001 - International Business Machines Corporation

Each particular table in packet processor 103 may comprise a table definition

 \dots in pipeline register 218A is transferred to CAM 211, the particular thread \dots

[APPLICATION] Providing access to data shared by packet processing threads

US Pat. App 11026652 - Filed Dec 30, 2004

For example, the CAM may store an ID of shared data (eg, a packet flow and, ...

When a core receives a shared data update message, a thread executing on the ...

[APPLICATION] Software controlled content addressable memory in a general purpose ...
US Pat. App 11043782 • Filed Jan 25, 2005

CRC support can be provided by replacing the first one of the packet processing

... a code section for which only one ME thread has exclusive modification ...

Method and system for managing traffic in a packet network environment

US Pat. 6781990 - Filed Feb 11, 2002 - Extreme Networks
5 The service index CAM 120 is the PECAM that maps the 56-bit VIP ... The PM

ontroller 1002 is the thread manager for the multiple sub-engines 1004-1008 ...

[APPLICATION] NETWORK PROCESSOR WITH SINGLE INTERFACE SUPPORTING TREE SEARCH ENGINE AND CAM

US Pat. App 11457952 - Filed Jul 17, 2006

As stated above, a thread number may refer to a particular task, eg, processing a particular packet of data, of processor 201. ...

Method and apparatus to process switch traffic

US Pat. 7324520 - Filed Jul 3, 2002 - Intel Corporation

In one embodiment, the micro- 15 engine content addressable memory (CAM) is used to

... On receiving an end of packet signal, the thread passes the 45 ...

[APPLICATION] Determining a checksum from packet data

US Pat. App 10692585 - Filed Oct 23, 2003

A thread arbiter 220 selects the program counter for a thread to execute. ...

CAM entries and access the data 256 associated with a packet having that base ...

[APPLICATION] Method for parallel processing of events within multiple event contexts ...

US Pat. App 10718497 - Filed Nov 19, 2003

5, when a thread is in binding state, the thread searches 70 the CAM for the ID

of the event context to which the event, eg, packet, that the thread is ...

[APPLICATION] Method and apparatus to assemble data segments into full packets for ...

US Pat. App 10188087 - Filed Jul 3, 2002 - INTEL CORPORATION

Conversely, on a CAM miss, the thread must evict the returned LRU re-assembly state

... 5, a data segment of a packet may be received 505 in RBUF 310 from, ...

(APPLICATION) Apparatus and method using vector table indirection to maintain forwarding ... US Pat. App 11015750 - Filed Dec 17, 2004 - SAMSUNG ELECTRONICS Co., LTD.

The microengine write function monitors each forwarding thread and sets its ...

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previous packet to the transition state at the start of the current packet. ...

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